IN THE SPECIFICATION

Please amend the paragraph beginning on page 3, line 9, as follows:

Fig. 5A and Fig. 5B are portions of ~~Fig. 5 is~~ a flowchart of a program translated from the program of Fig. 4 according to some embodiments of the present invention.

Please amend the paragraph beginning on page 10, line 20, as follows:

In some embodiments, the parallel threads may execute the instructions in block 513. These instructions, in some embodiments, may cause the parallel threads to set up descriptors such as 801 in Figure 8, and may specify upper and lower bounds of a private array to be ~~copier~~ copied. In block 513, "CPR1.F0" may indicate a base address for the private array while "CPR1.LB_F0_1" may indicate a lower bound and "CPR1.UB_F0_1" may indicate an upper bound of the private array.